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FIG. 1

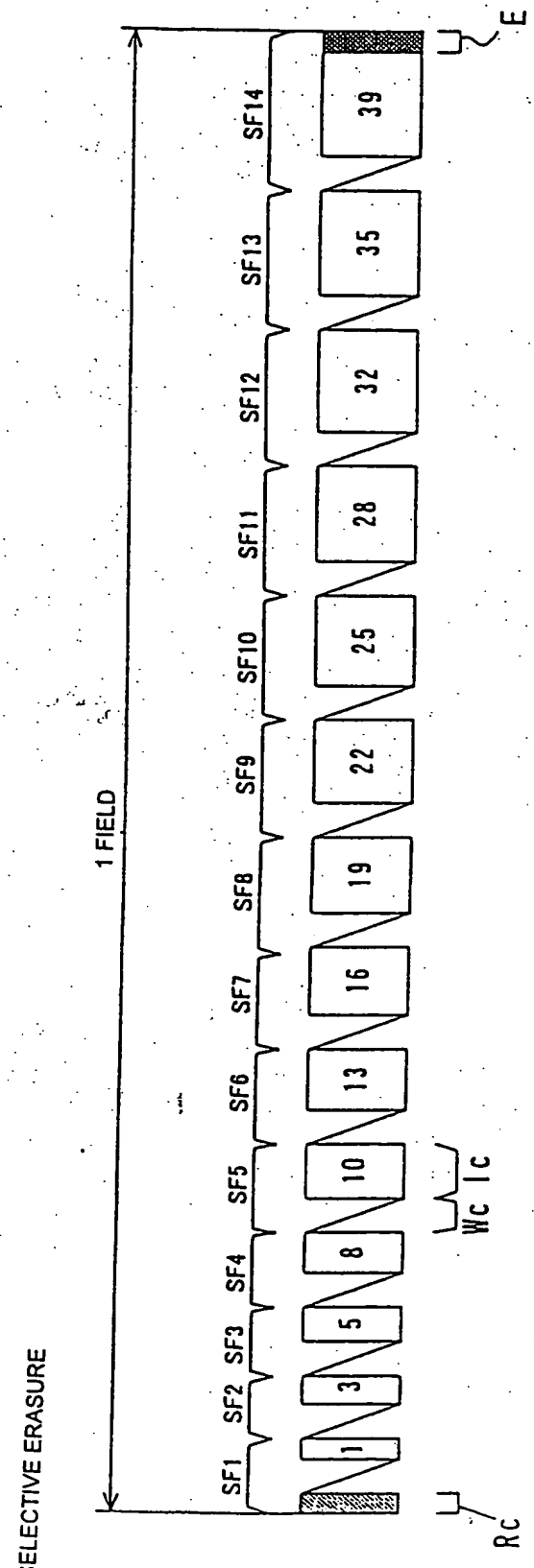

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FIG. 2

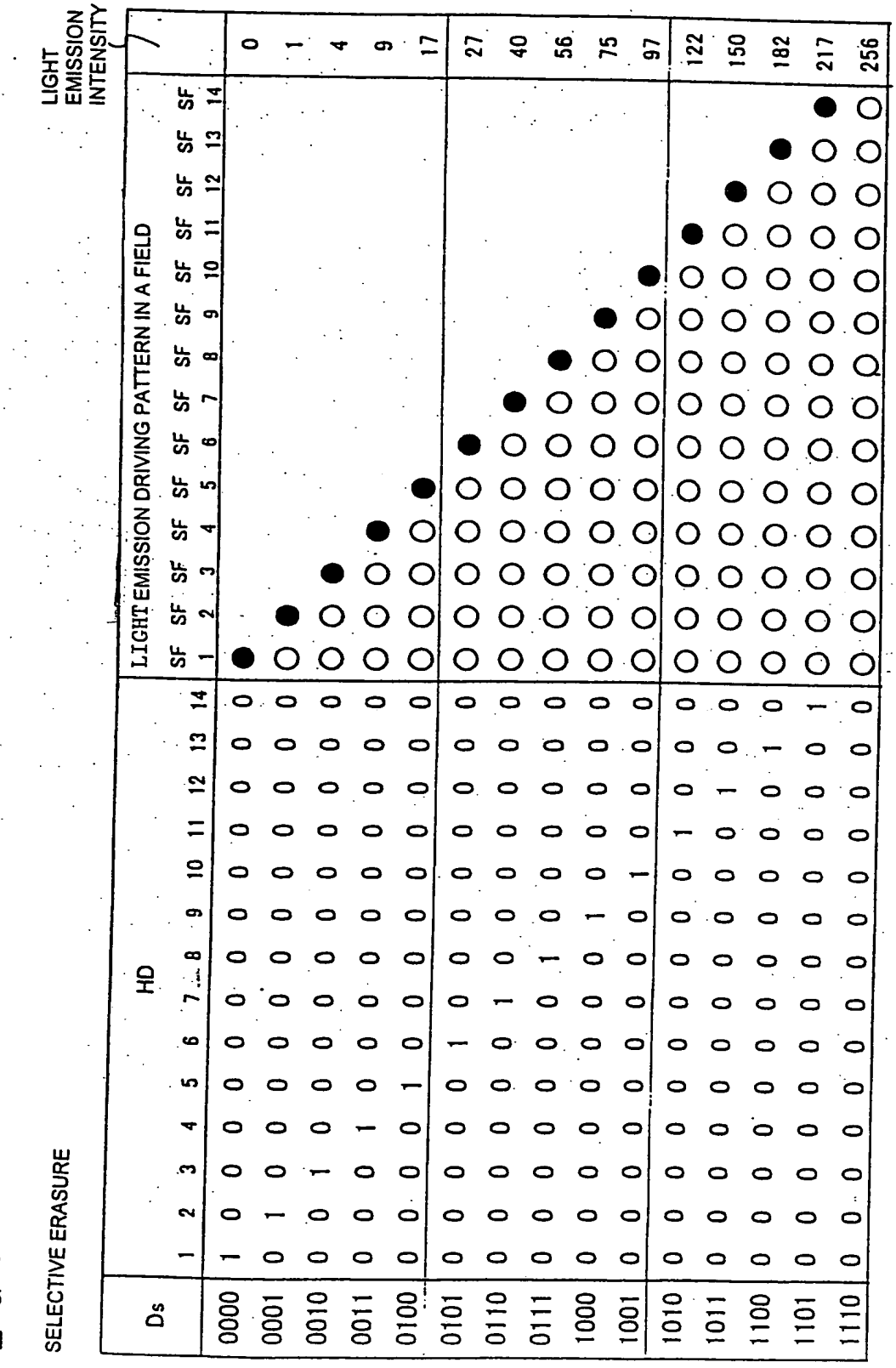

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FIG. 3

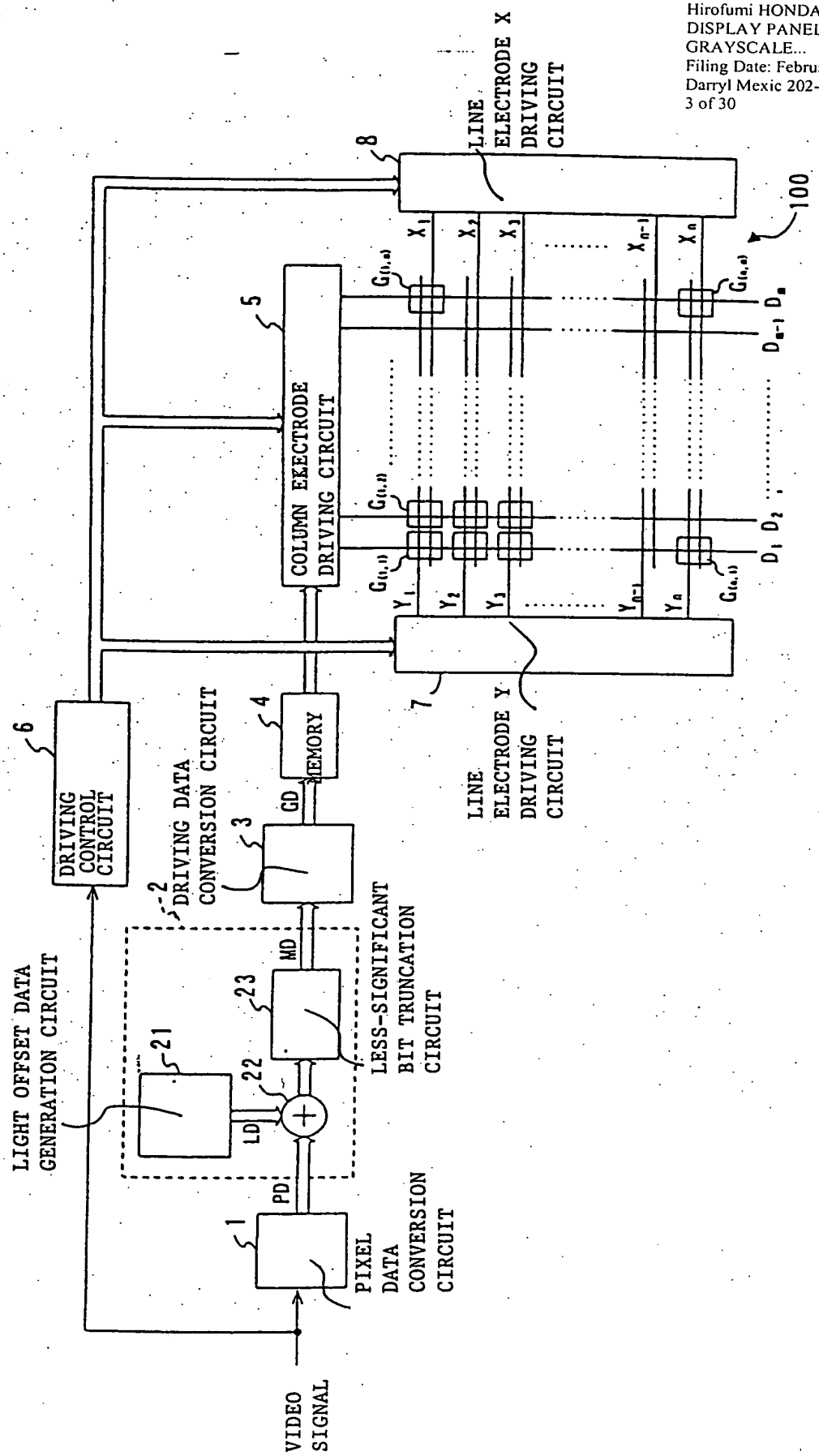

FIG. 4

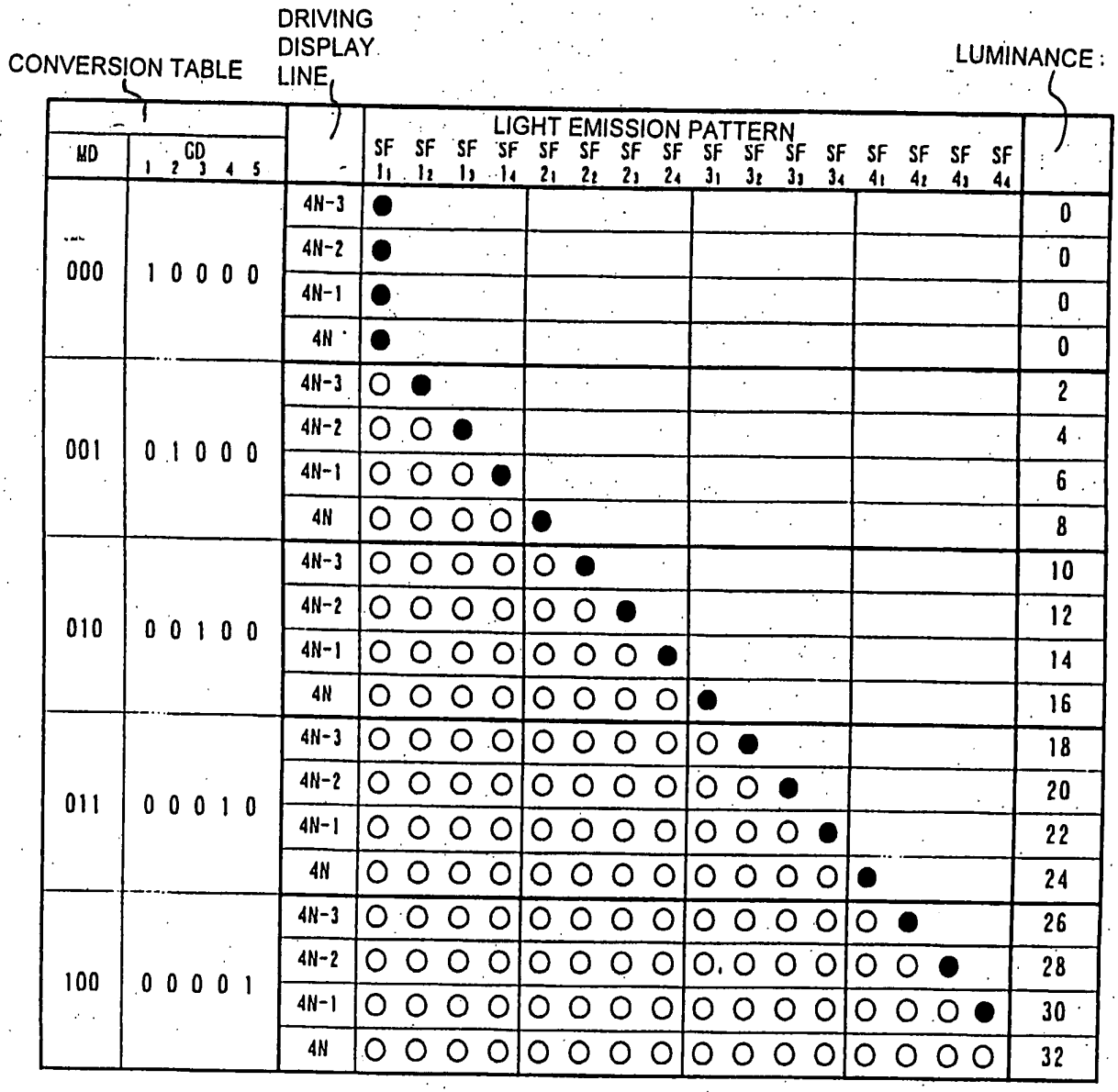

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FIG. 5

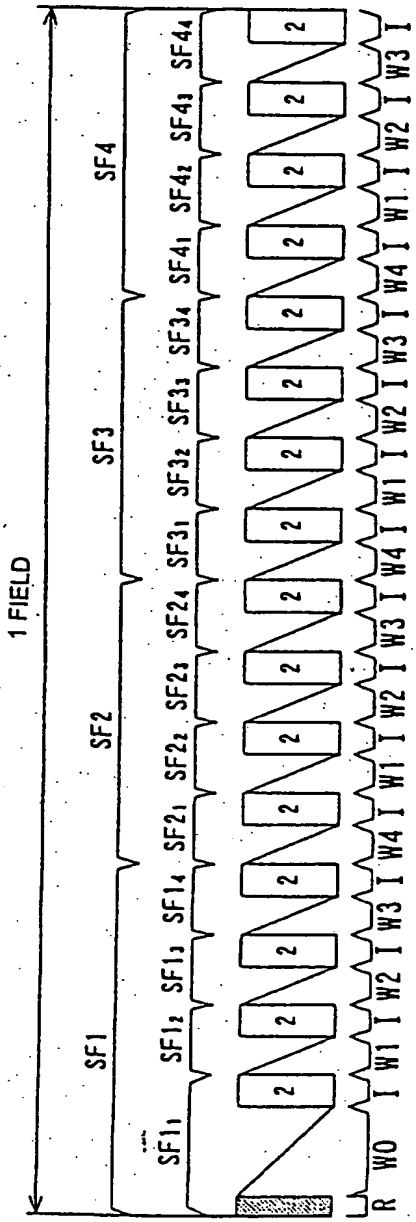

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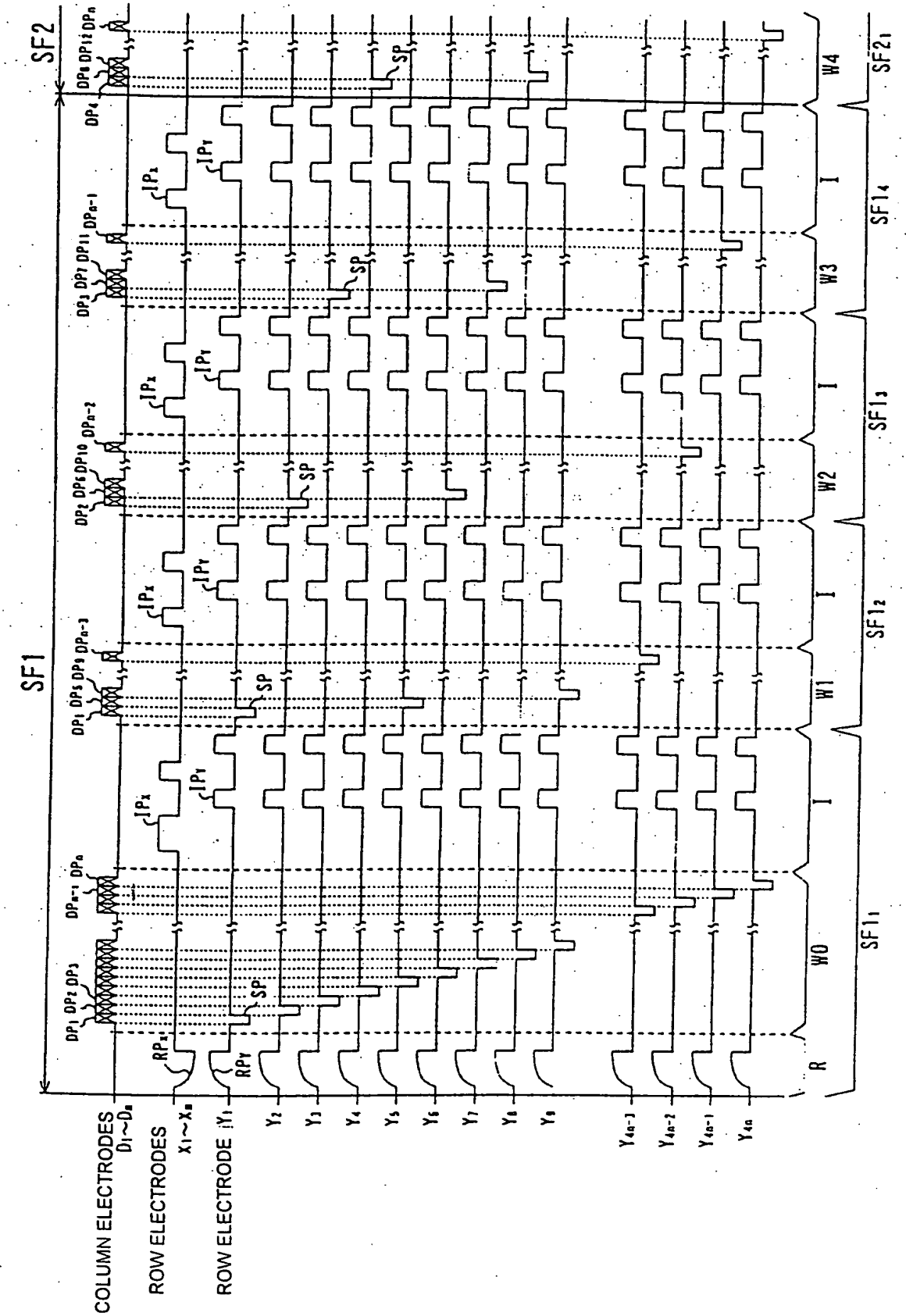
FIG. 6



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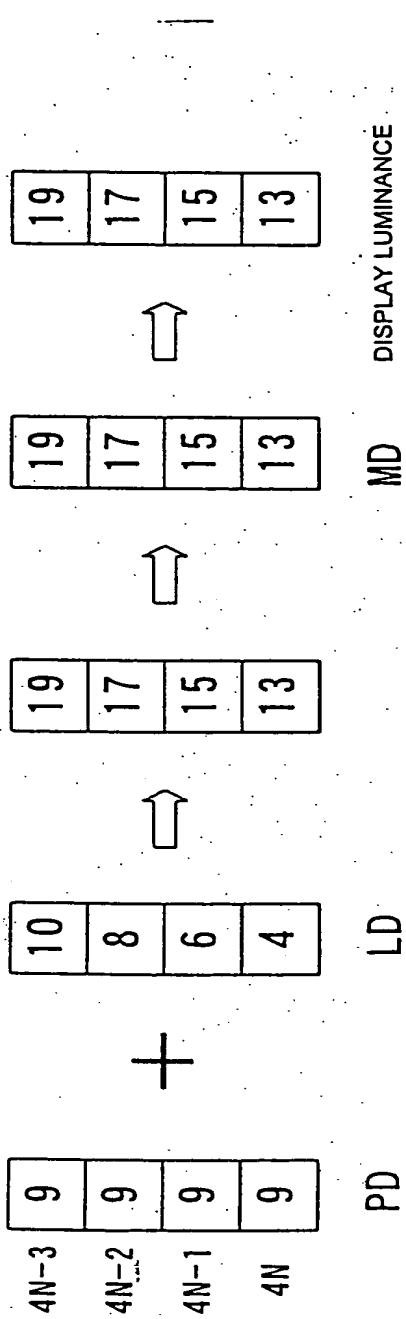

FIG. 7

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FIG. 8

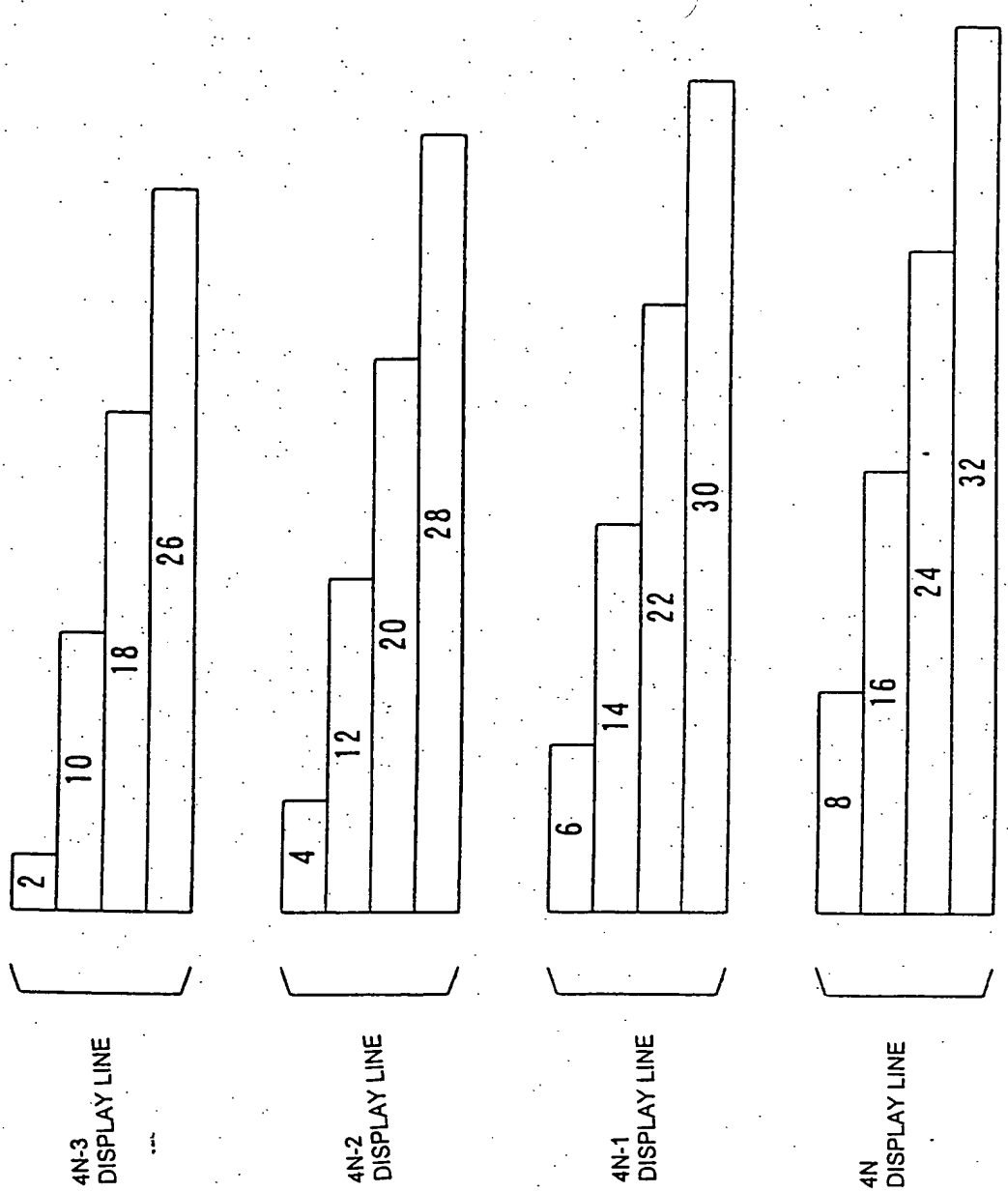

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FIG. 9

FIG. 10

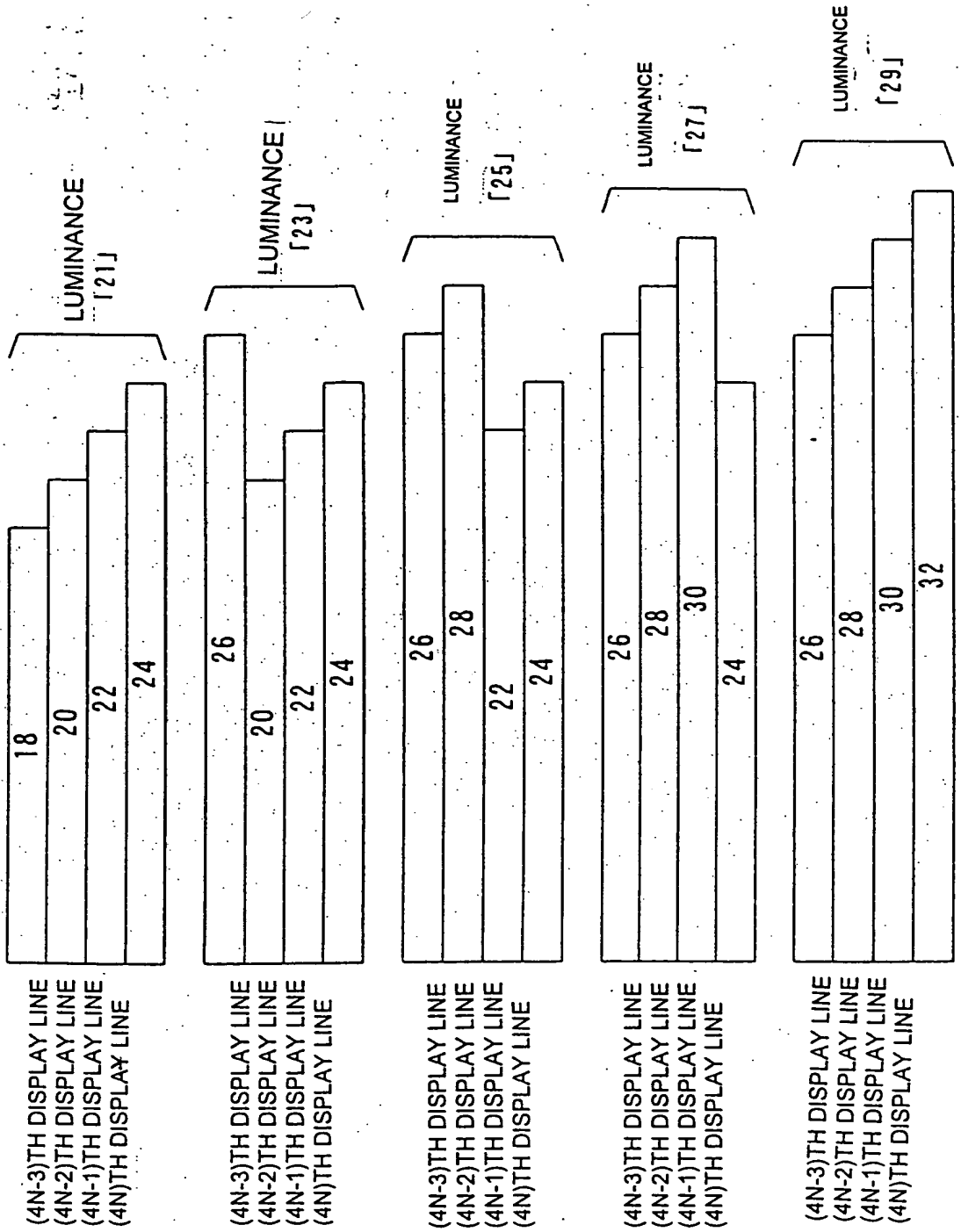
LUMINANCE [21]
18
20
22
24
(4N-3)TH DISPLAY LINE
(4N-2)TH DISPLAY LINE
(4N-1)TH DISPLAY LINE
(4N)TH DISPLAY LINE
LUMINANCE [23]
26
20
22
24
(4N-3)TH DISPLAY LINE
(4N-2)TH DISPLAY LINE
(4N-1)TH DISPLAY LINE
(4N)TH DISPLAY LINE
LUMINANCE [25]
26
28
22
24
(4N-3)TH DISPLAY LINE
(4N-2)TH DISPLAY LINE
(4N-1)TH DISPLAY LINE
(4N)TH DISPLAY LINE
LUMINANCE [27]
26
28
30
24
(4N-3)TH DISPLAY LINE
(4N-2)TH DISPLAY LINE
(4N-1)TH DISPLAY LINE
(4N)TH DISPLAY LINE
LUMINANCE [29]
26
28
30
32
(4N-3)TH DISPLAY LINE
(4N-2)TH DISPLAY LINE
(4N-1)TH DISPLAY LINE
(4N)TH DISPLAY LINE

FIG. 11

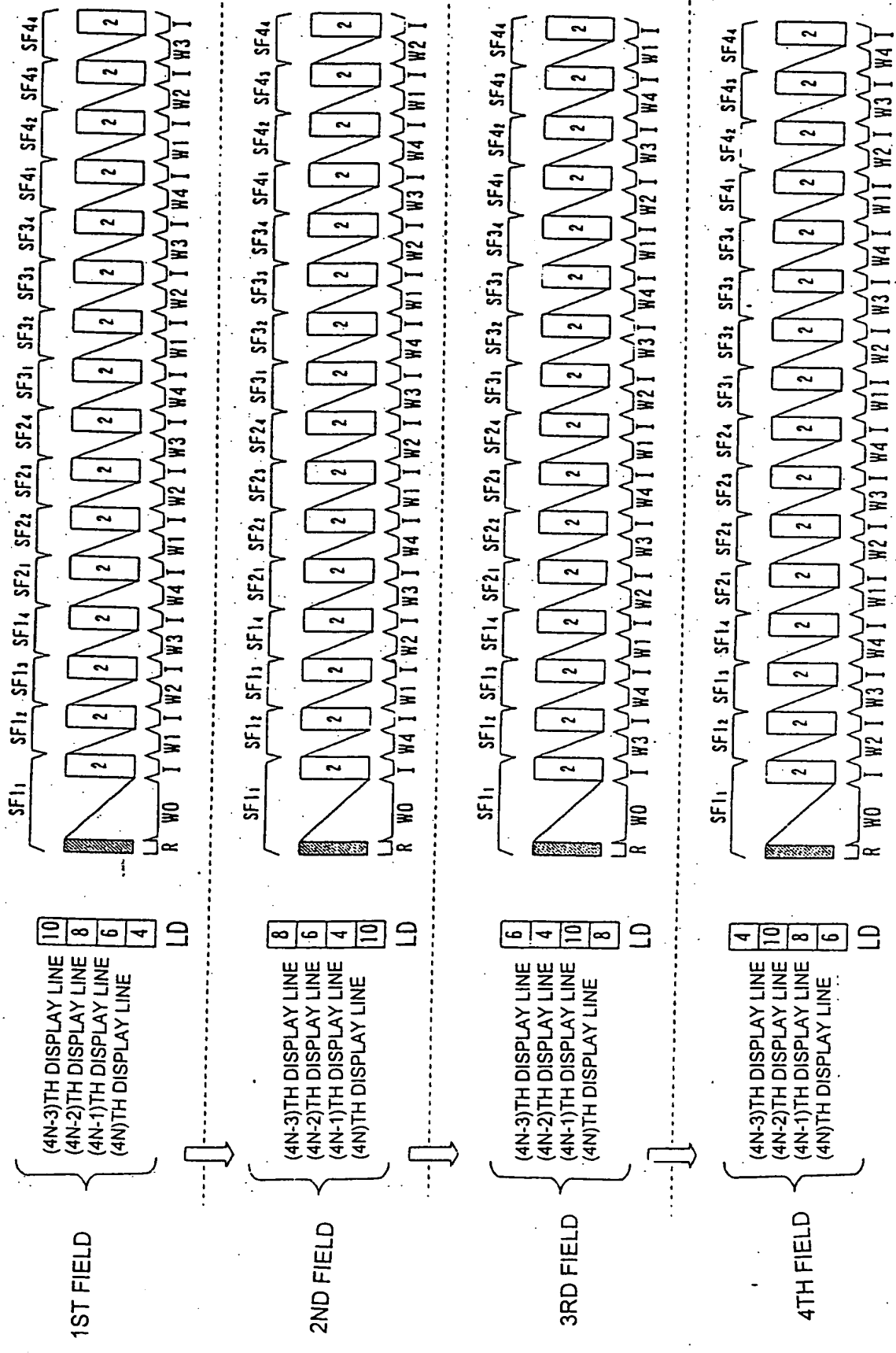
1ST FIELD
2ND FIELD
3RD FIELD
4TH FIELD
(4N-3)TH DISPLAY LINE
(4N-2)TH DISPLAY LINE
(4N-1)TH DISPLAY LINE
(4N)TH DISPLAY LINE
LD

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FIG. 12

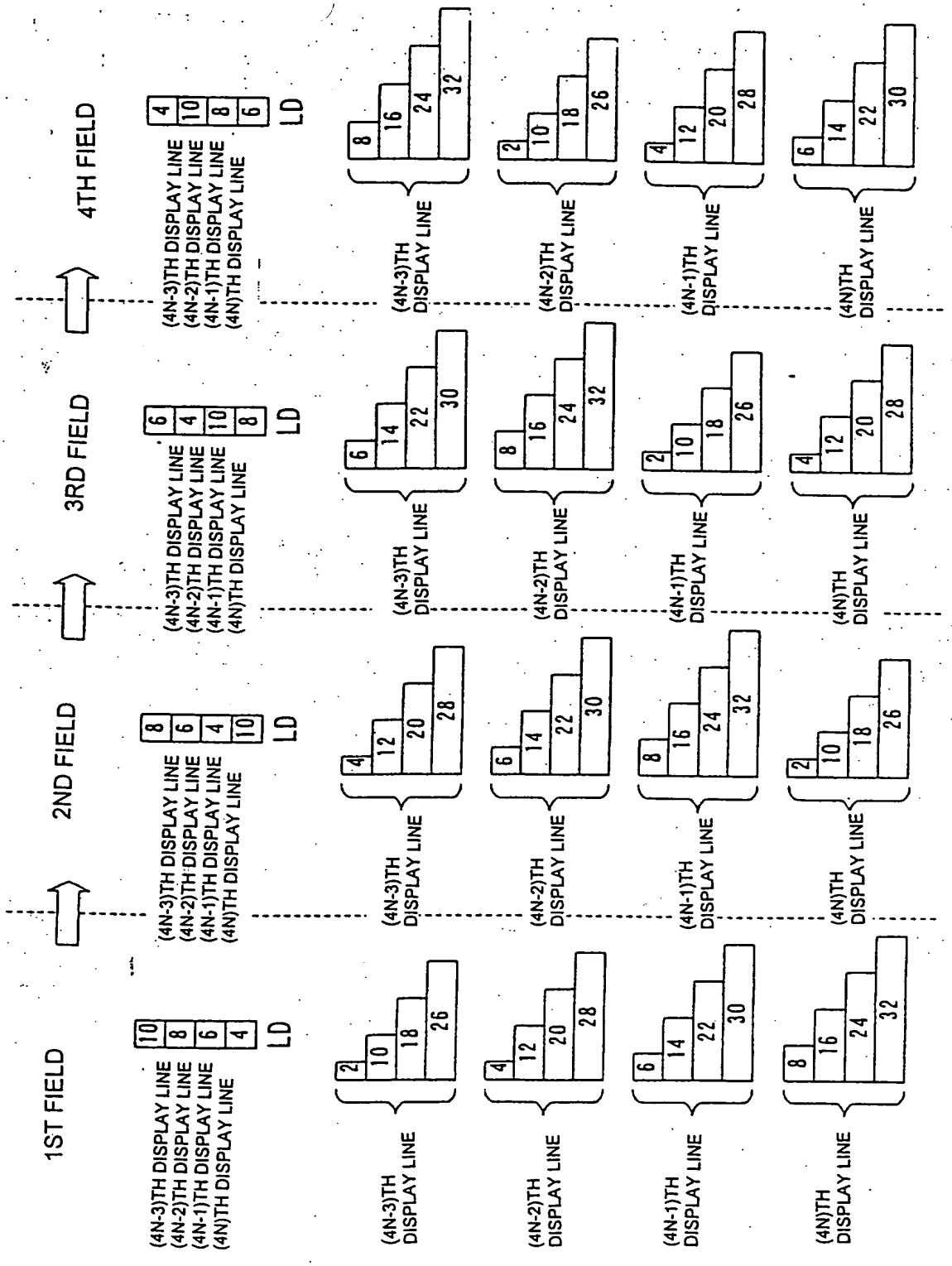

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FIG. 13

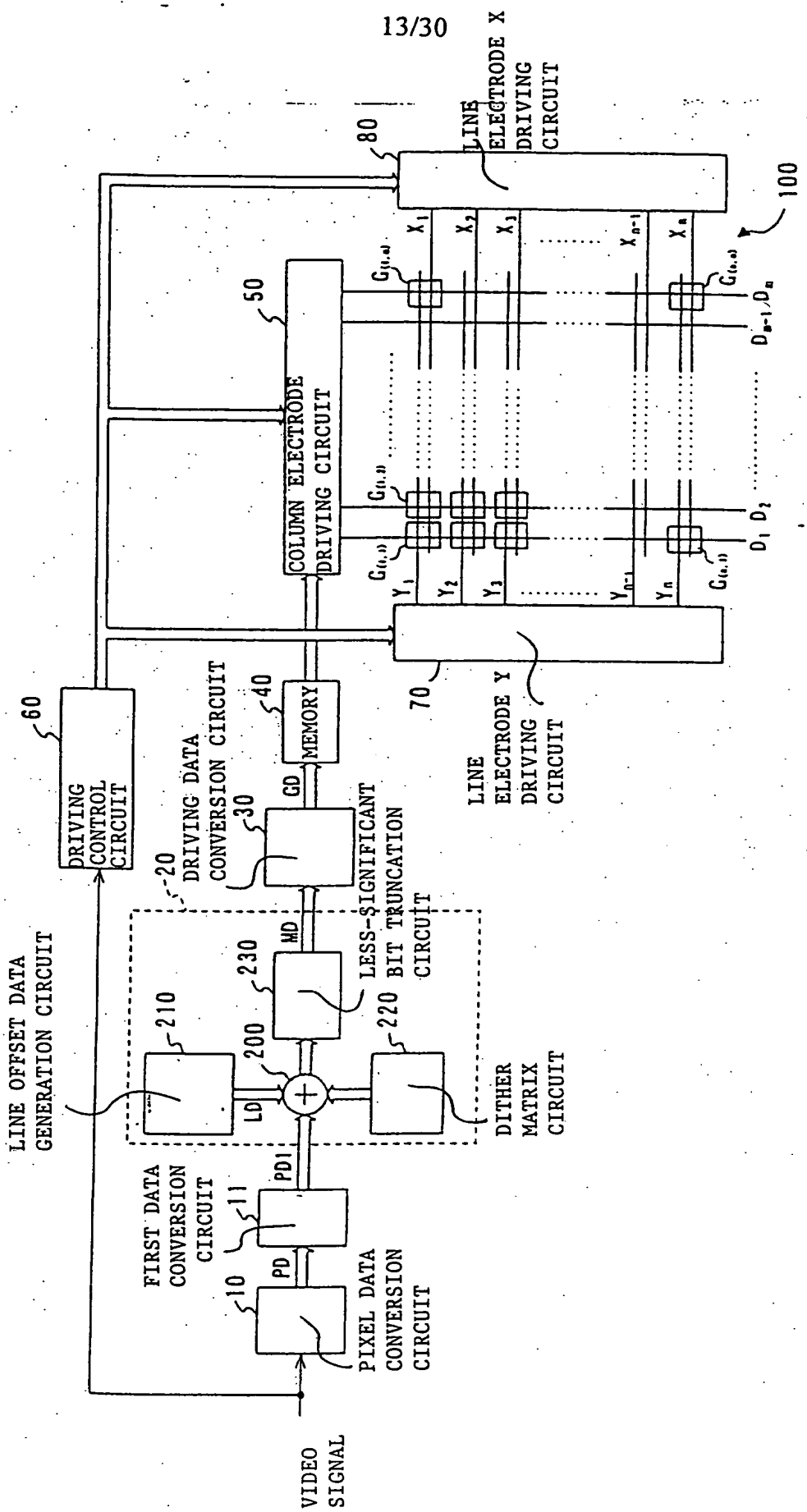

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FIG. 14

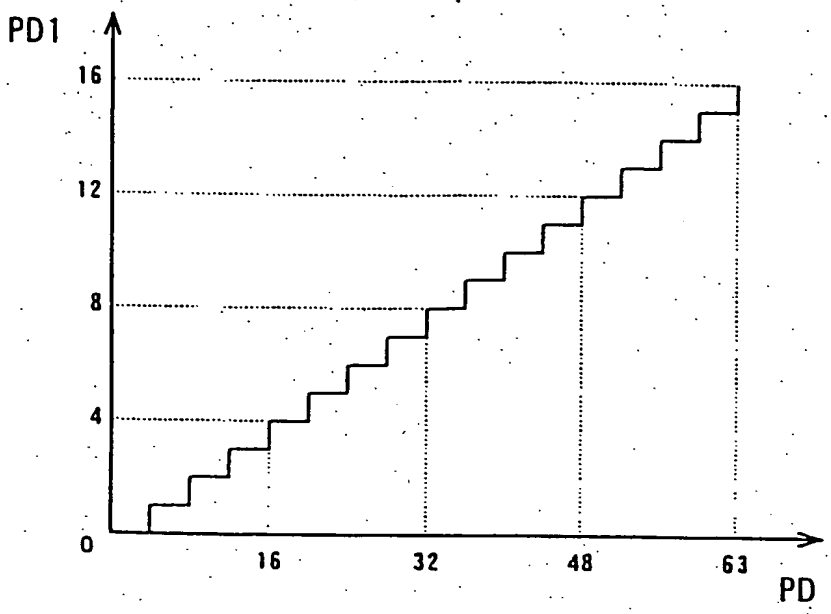

FIG. 15

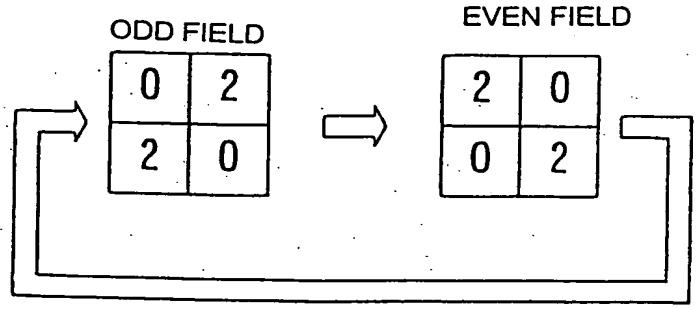

FIG. 16

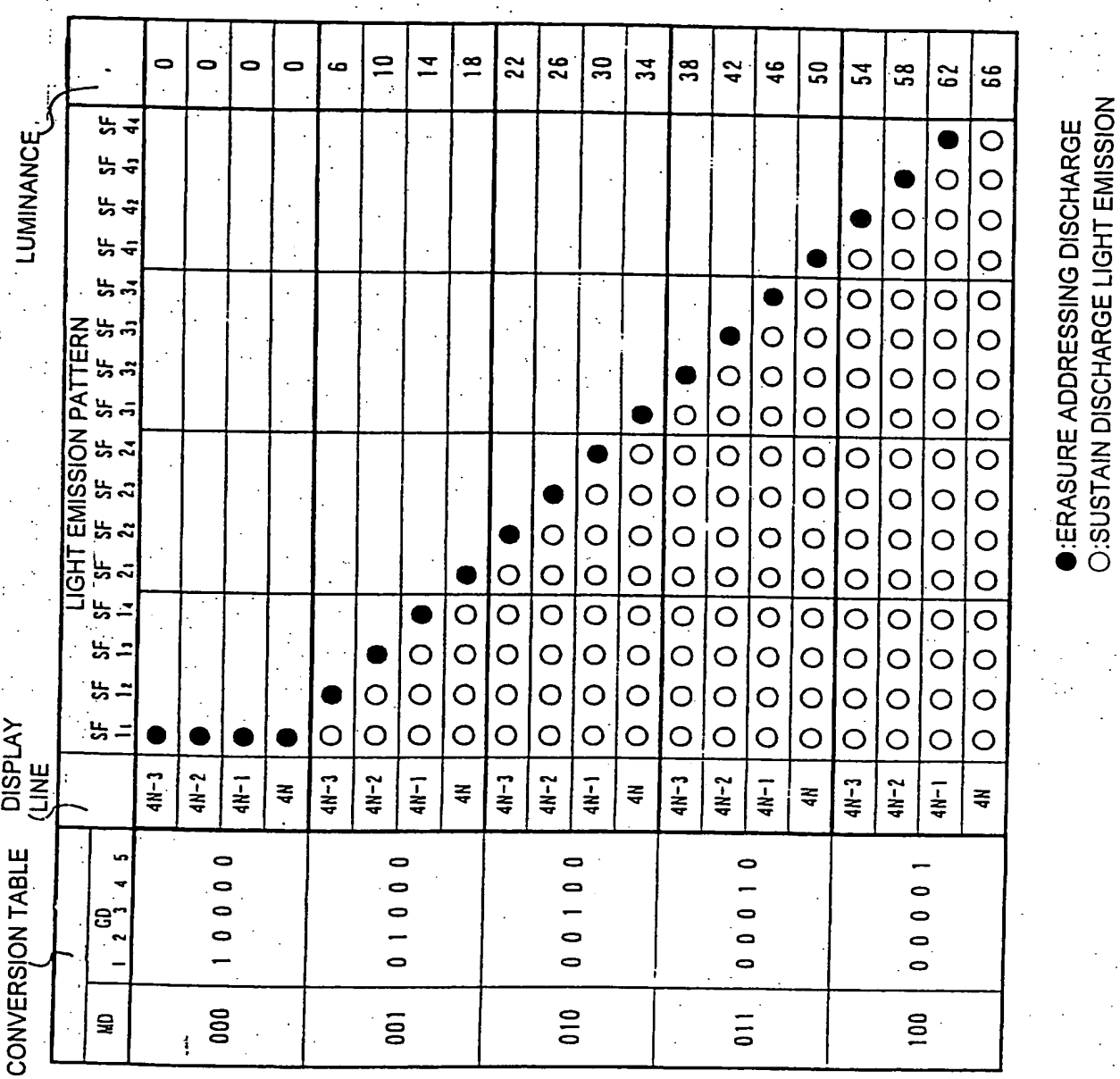
CONVERSION TABLE
MD
GD 1 2 3 4 5
DRIVING DISPLAY (LINE)
LIGHT EMISSION PATTERN
SF 1₁ SF 1₂ SF 1₃ SF 1₄ SF 2₁ SF 2₂ SF 2₃ SF 2₄ SF 3₁ SF 3₂ SF 3₃ SF 3₄ SF 4₁ SF 4₂ SF 4₃ SF 4₄
LUMINANCE
000 10000
001 01000
010 00100
011 00010
100 00001
4N-3
4N-2
4N-1
4N
0
6
10
14
18
22
26
30
34
38
42
46
50
54
58
62
66
●:ERASURE ADDRESSING DISCHARGE
○:SUSTAIN DISCHARGE LIGHT EMISSION

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FIG. 17

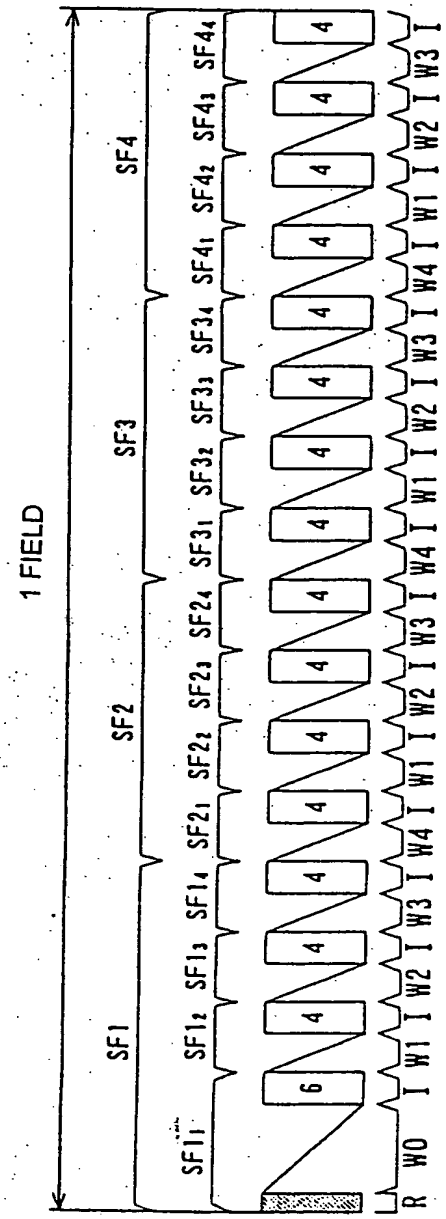

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FIG. 18

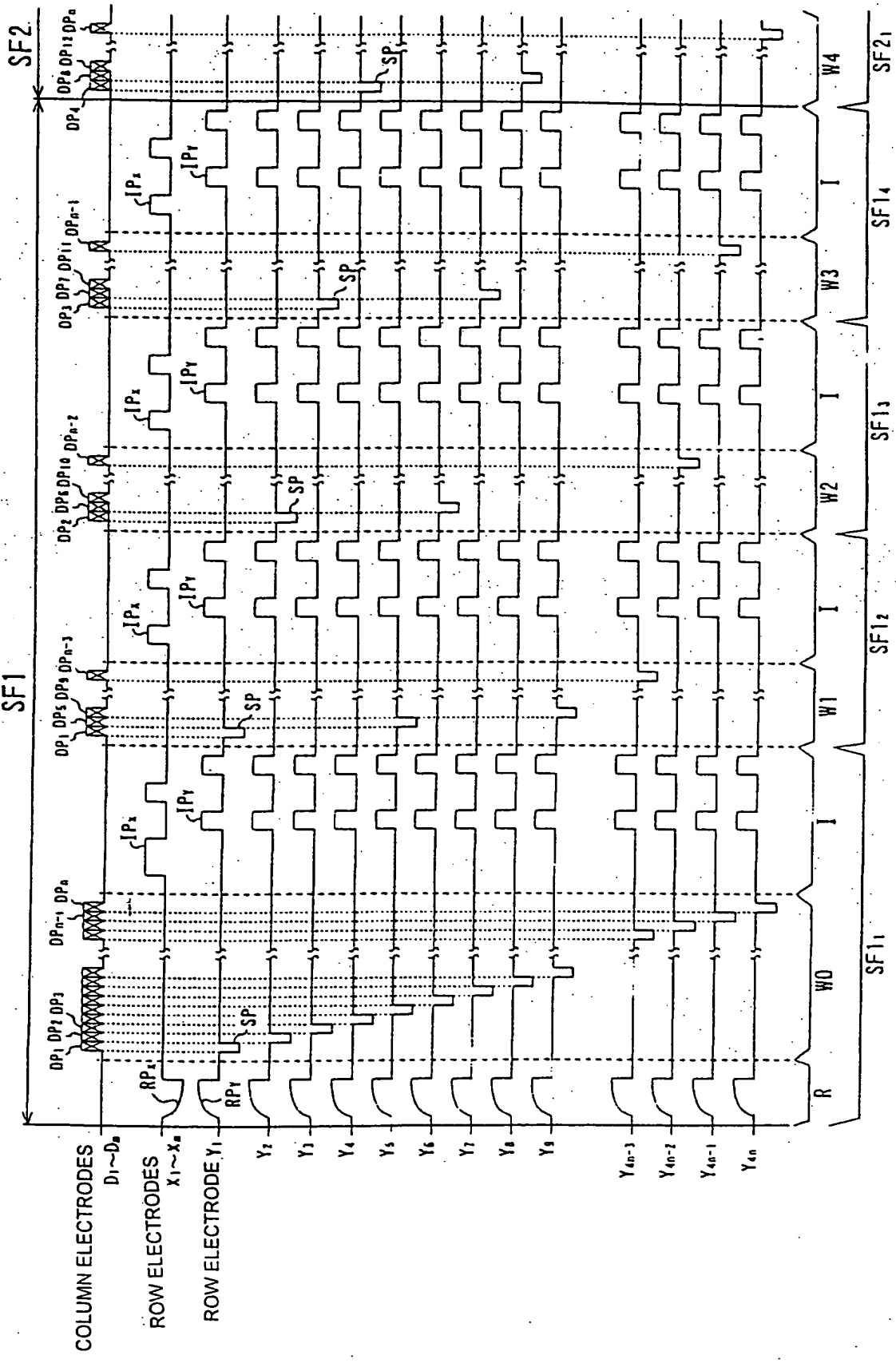

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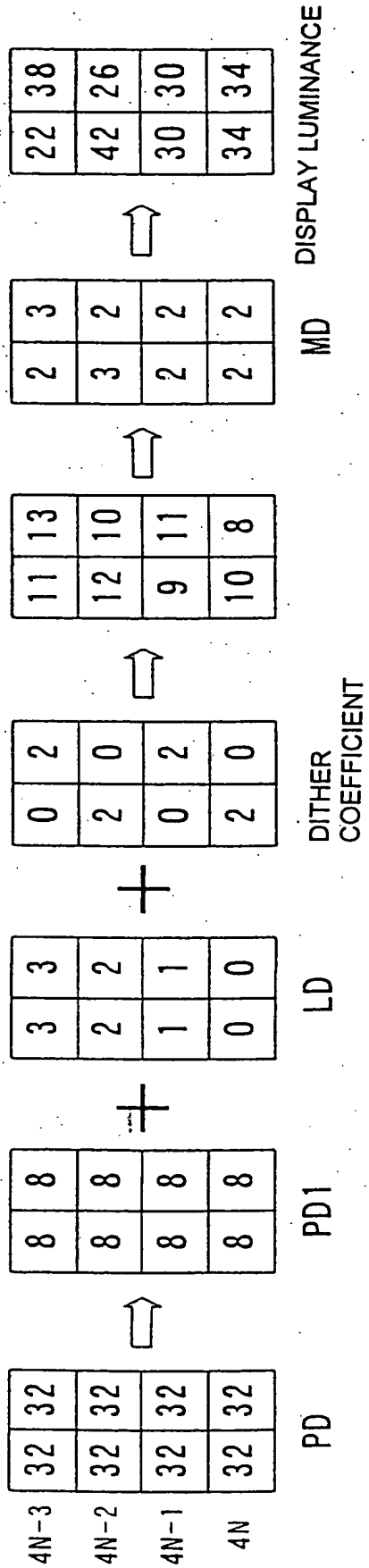
FIG. 19



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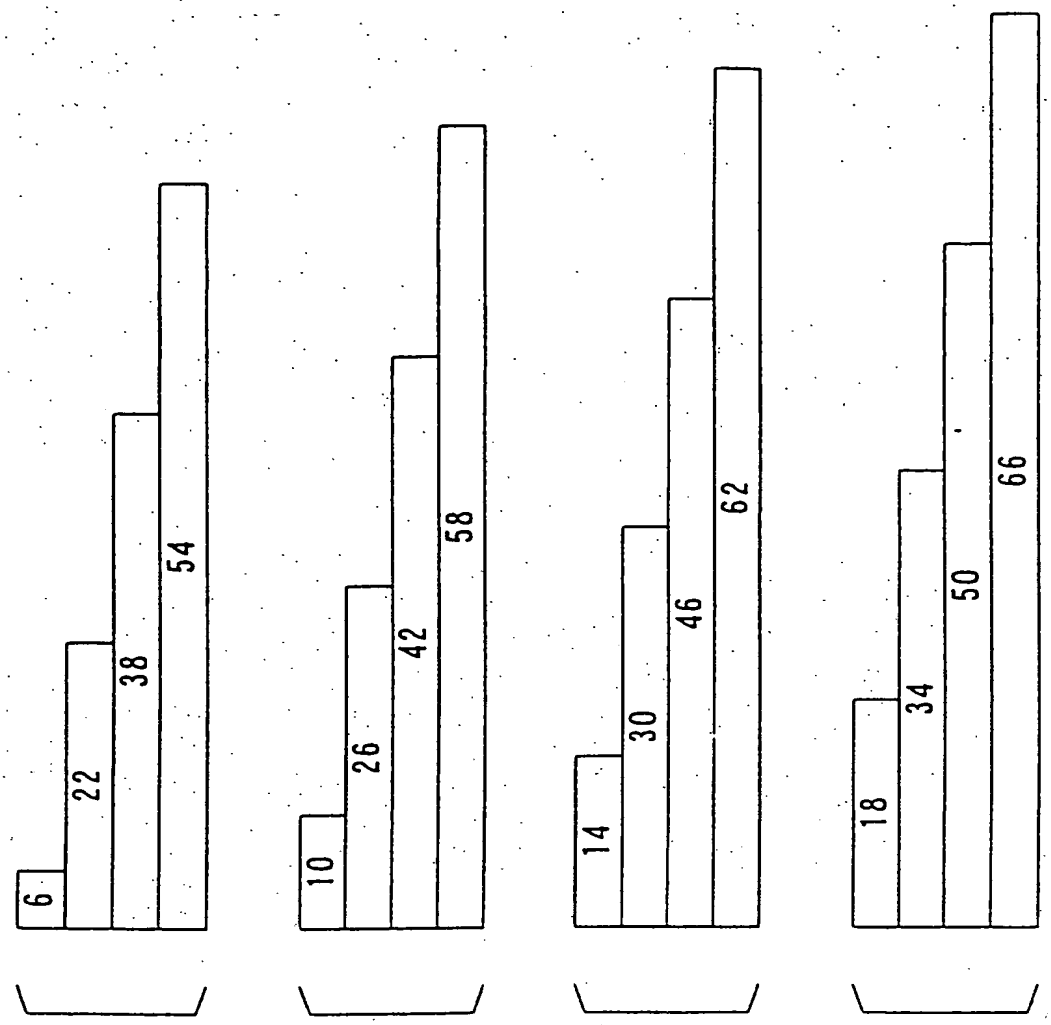

FIG. 20

FIG. 21

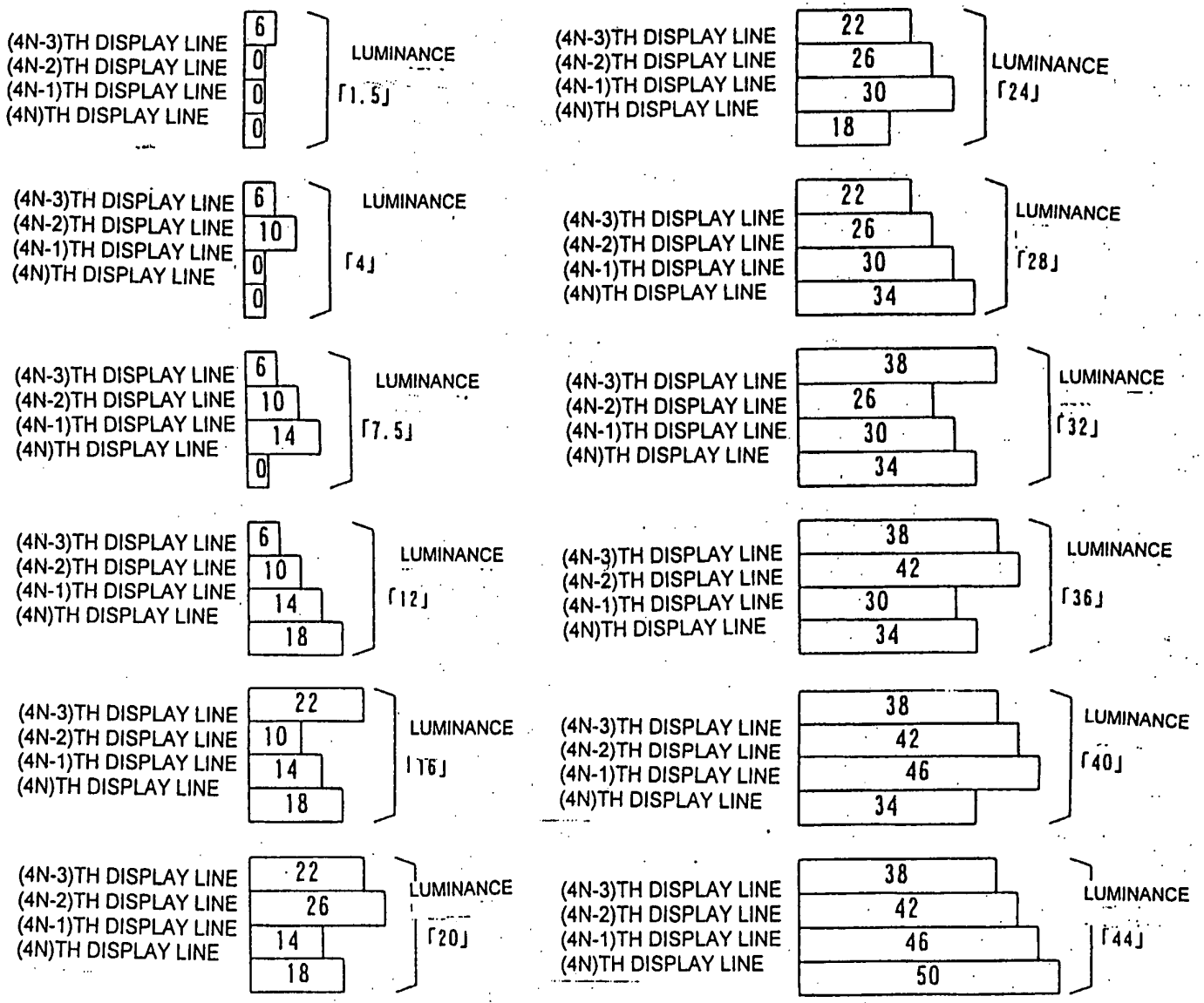

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FIG. 22

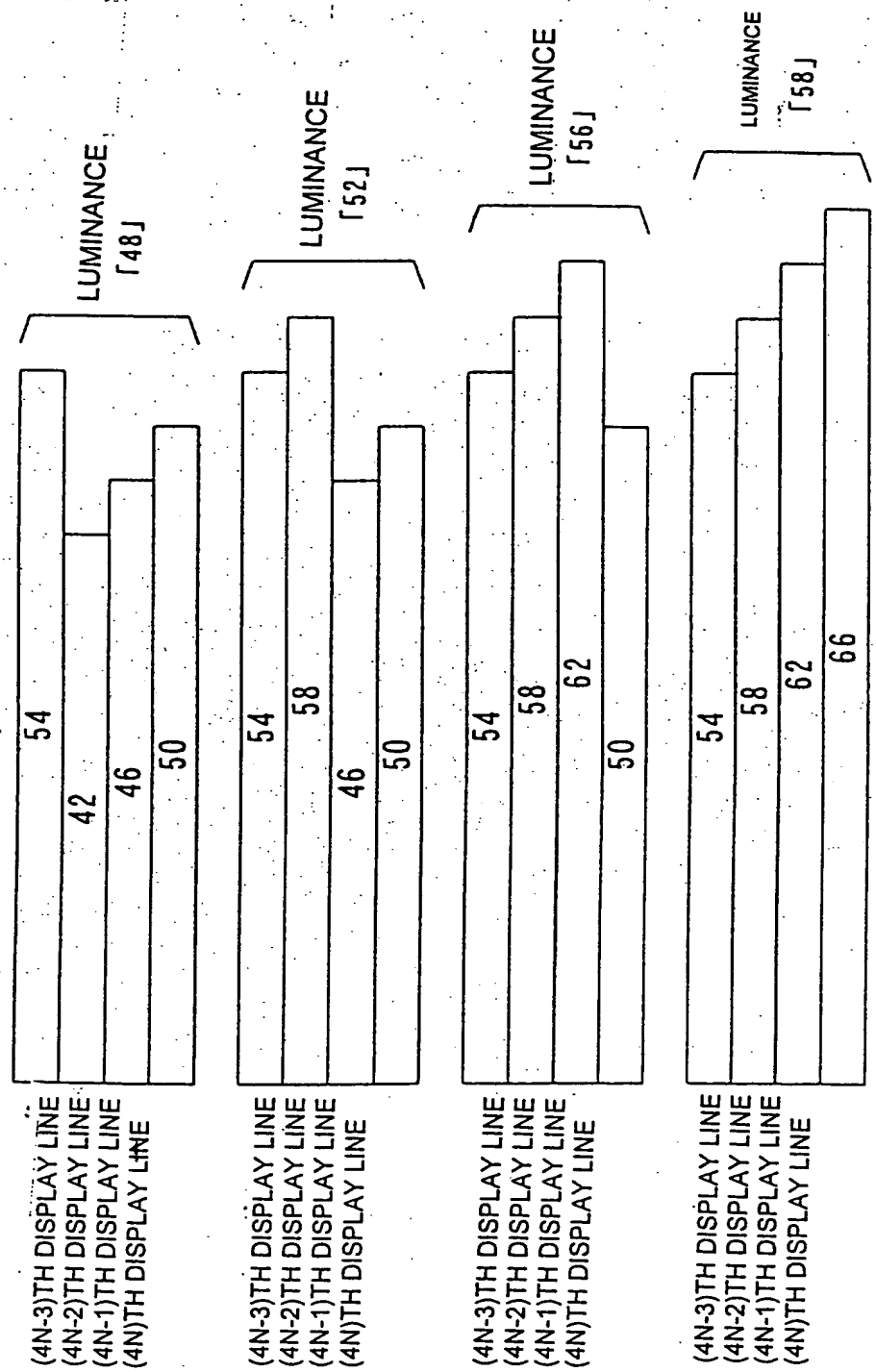
LUMINANCE [48]
LUMINANCE [52]
LUMINANCE [56]
LUMINANCE [58]
54
42
46
50
54
58
46
50
54
58
62
50
54
58
62
66
(4N-3)TH DISPLAY LINE
(4N-2)TH DISPLAY LINE
(4N-1)TH DISPLAY LINE
(4N)TH DISPLAY LINE

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FIG. 23

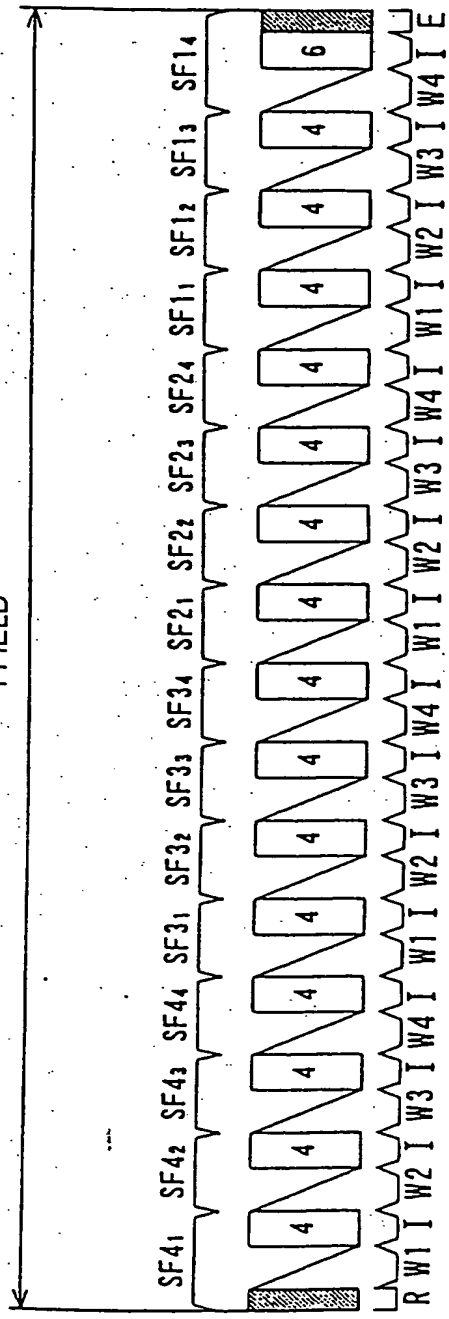

FIG. 24

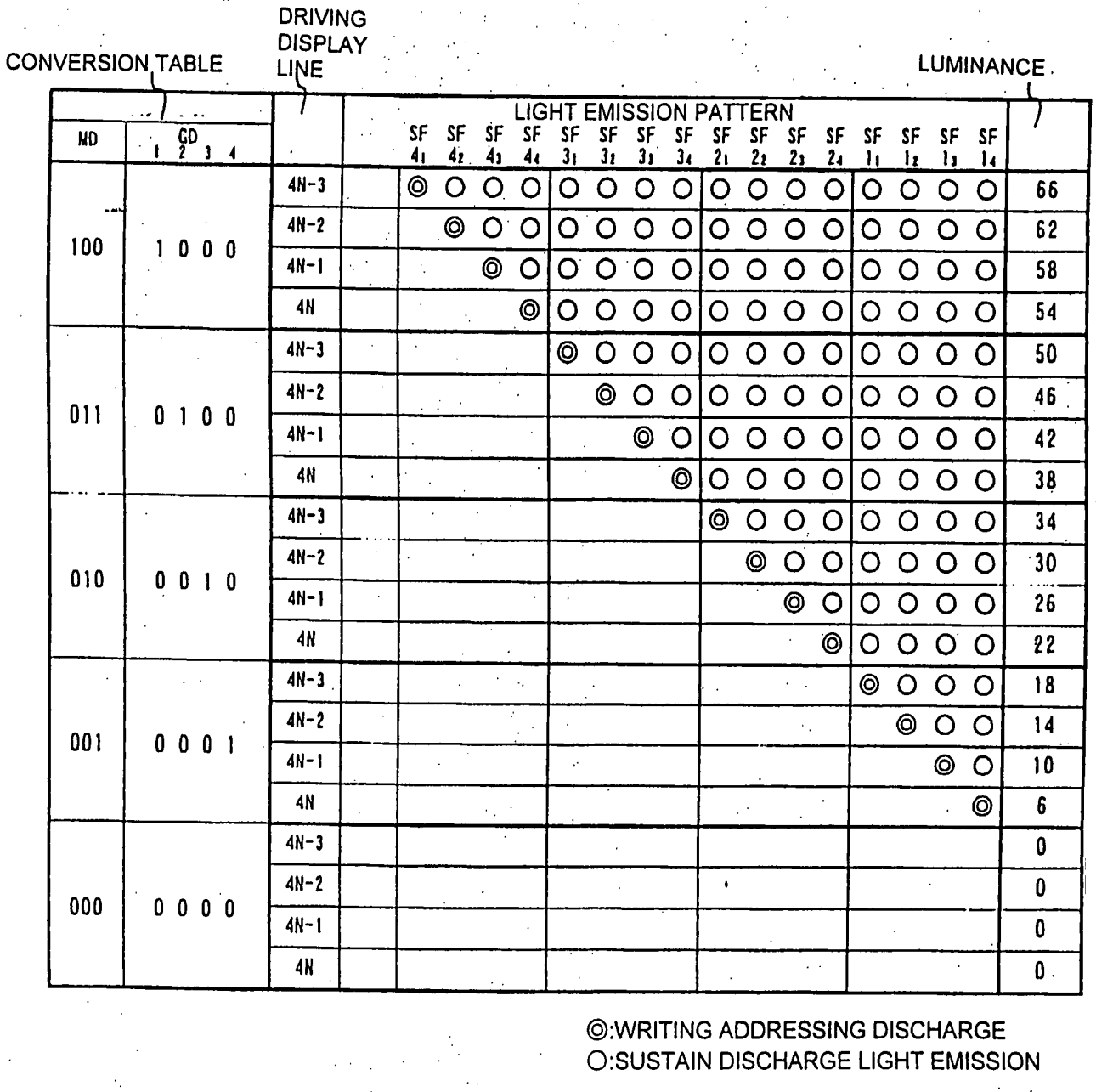

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FIG. 25

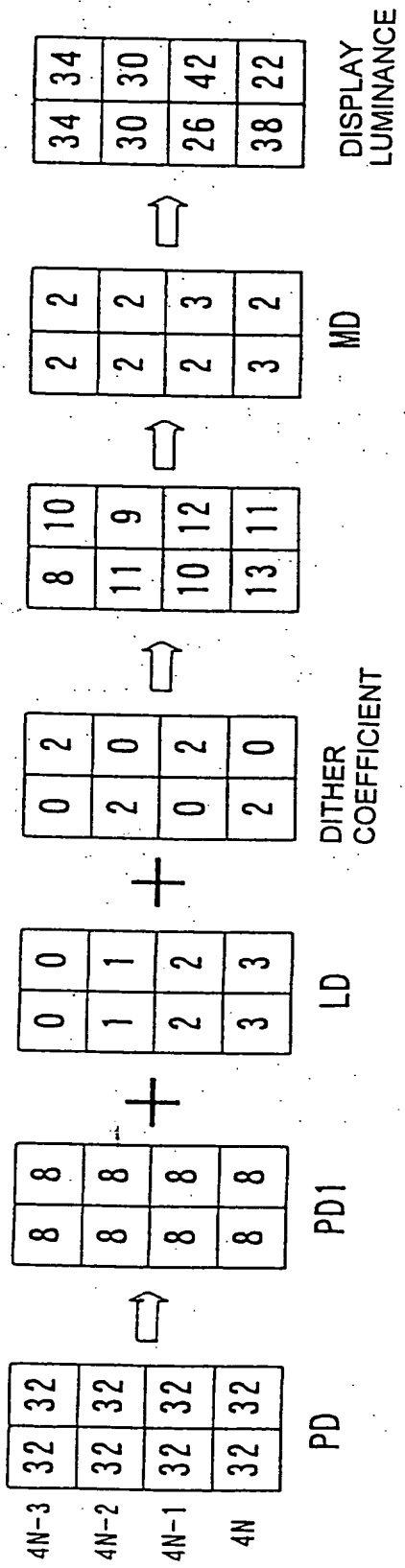

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FIG. 26

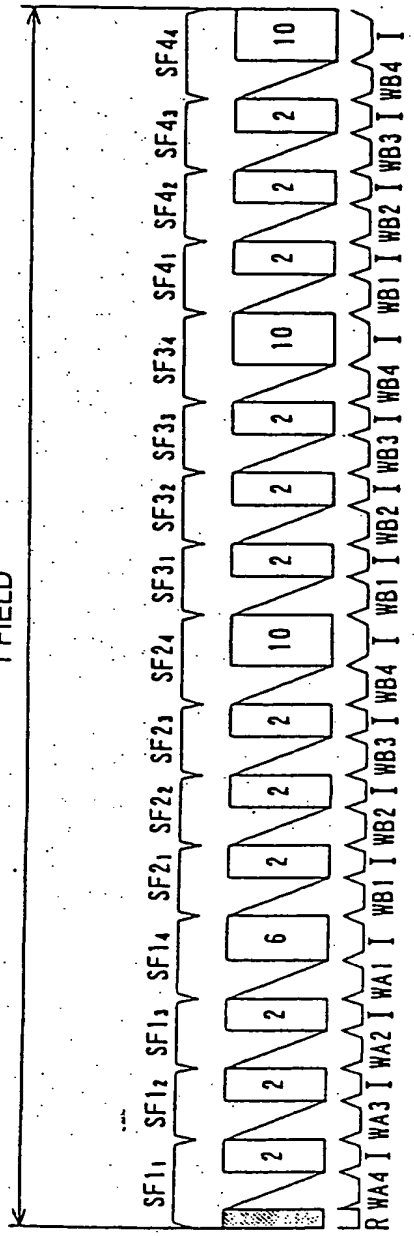

FIG. 27

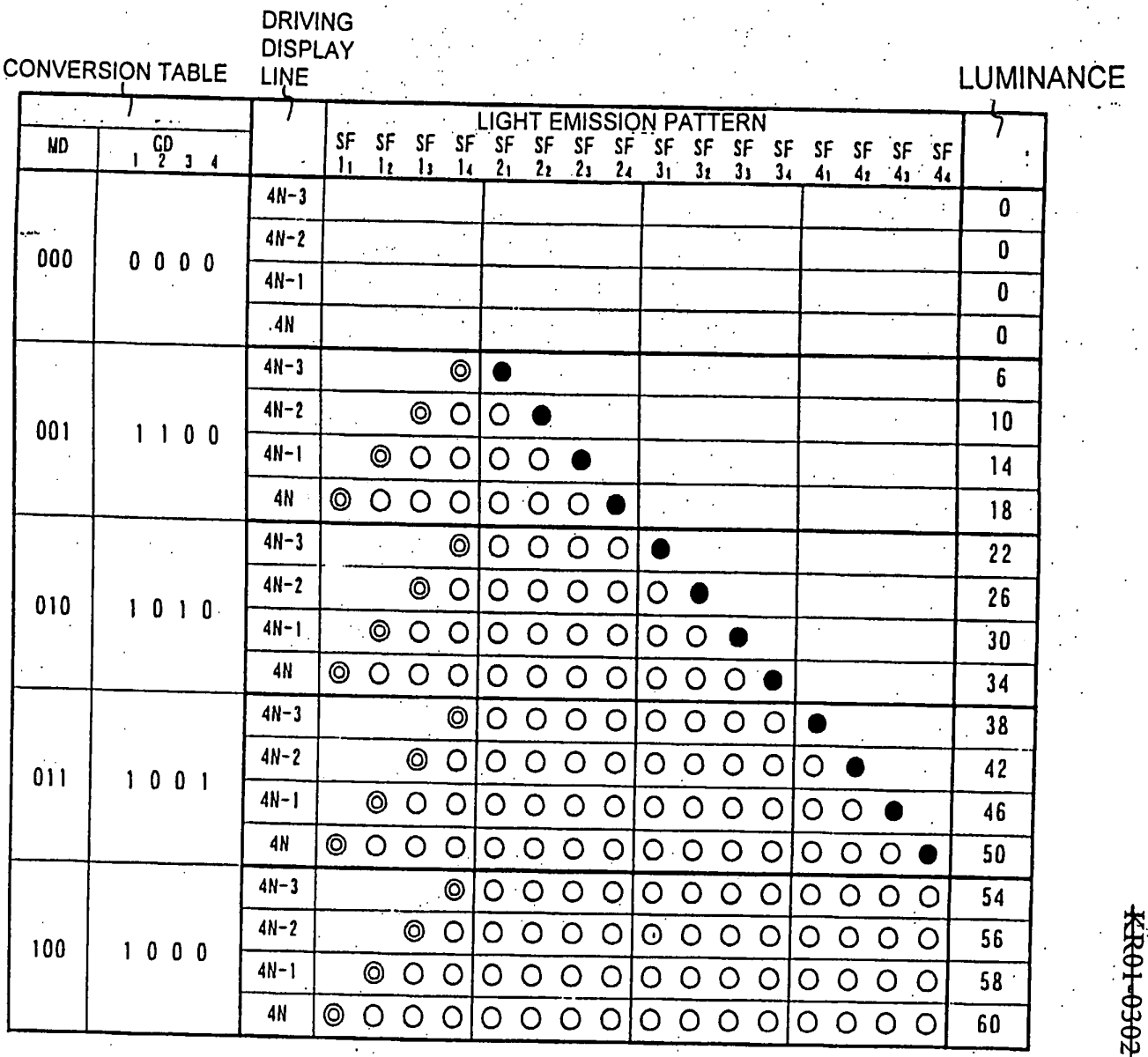

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FIG. 28

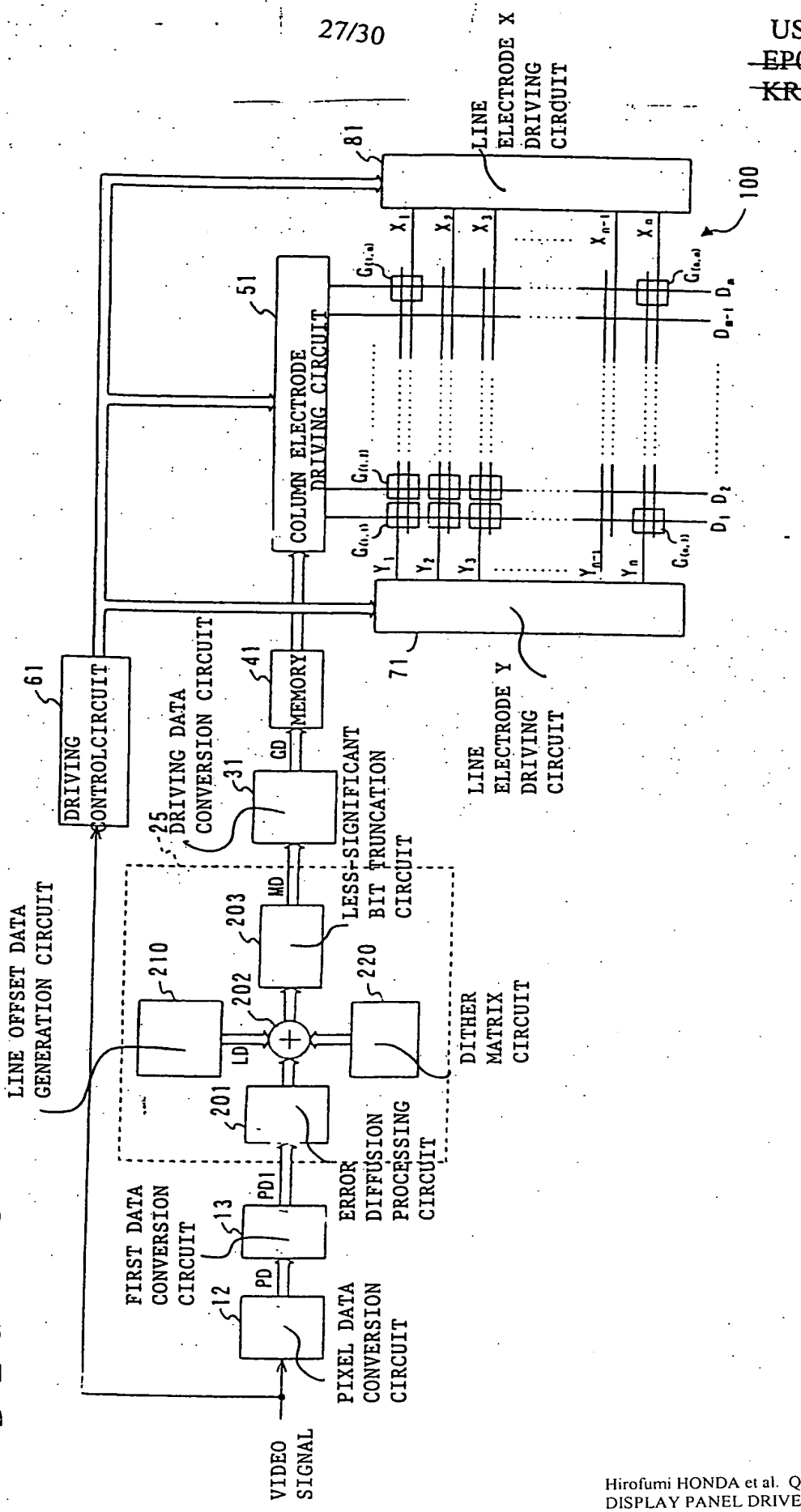
LINE ELECTRODE X DRIVING CIRCUIT
81
100
X1
X2
X3
Xn-1
Xn
G(1,n)
G(n,n)
Dn
Dn-1
51
COLUMN ELECTRODE DRIVING CIRCUIT
G(1,1)
G(n,1)
D1
D2
Y1
Y2
Y3
Yn-1
Yn
71
LINE ELECTRODE Y DRIVING CIRCUIT
61
DRIVING CONTROLCIRCUIT
41
MEMORY
GD
31
25
DRIVING DATA CONVERSION CIRCUIT
LESS-SIGNIFICANT BIT TRUNCATION CIRCUIT
MD
203
210
202
220
LD
DITHER MATRIX CIRCUIT
LINE OFFSET DATA GENERATION CIRCUIT
201
ERROR DIFFUSION PROCESSING CIRCUIT
PD1
13
FIRST DATA CONVERSION CIRCUIT
PD
12
PIXEL DATA CONVERSION CIRCUIT
VIDEO SIGNAL

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FIG. 29

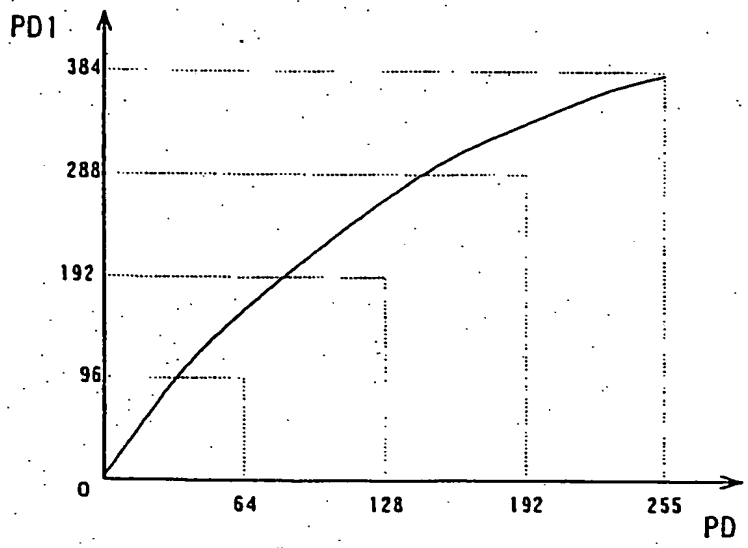

FIG. 30

(8N-7)TH DISPLAY LINE	0
(8N-6)TH DISPLAY LINE	4
(8N-5)TH DISPLAY LINE	8
(8N-4)TH DISPLAY LINE	12
(8N-3)TH DISPLAY LINE	16
(8N-2)TH DISPLAY LINE	20
(8N-1)TH DISPLAY LINE	24
(8N)TH DISPLAY LINE	28

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FIG. 31

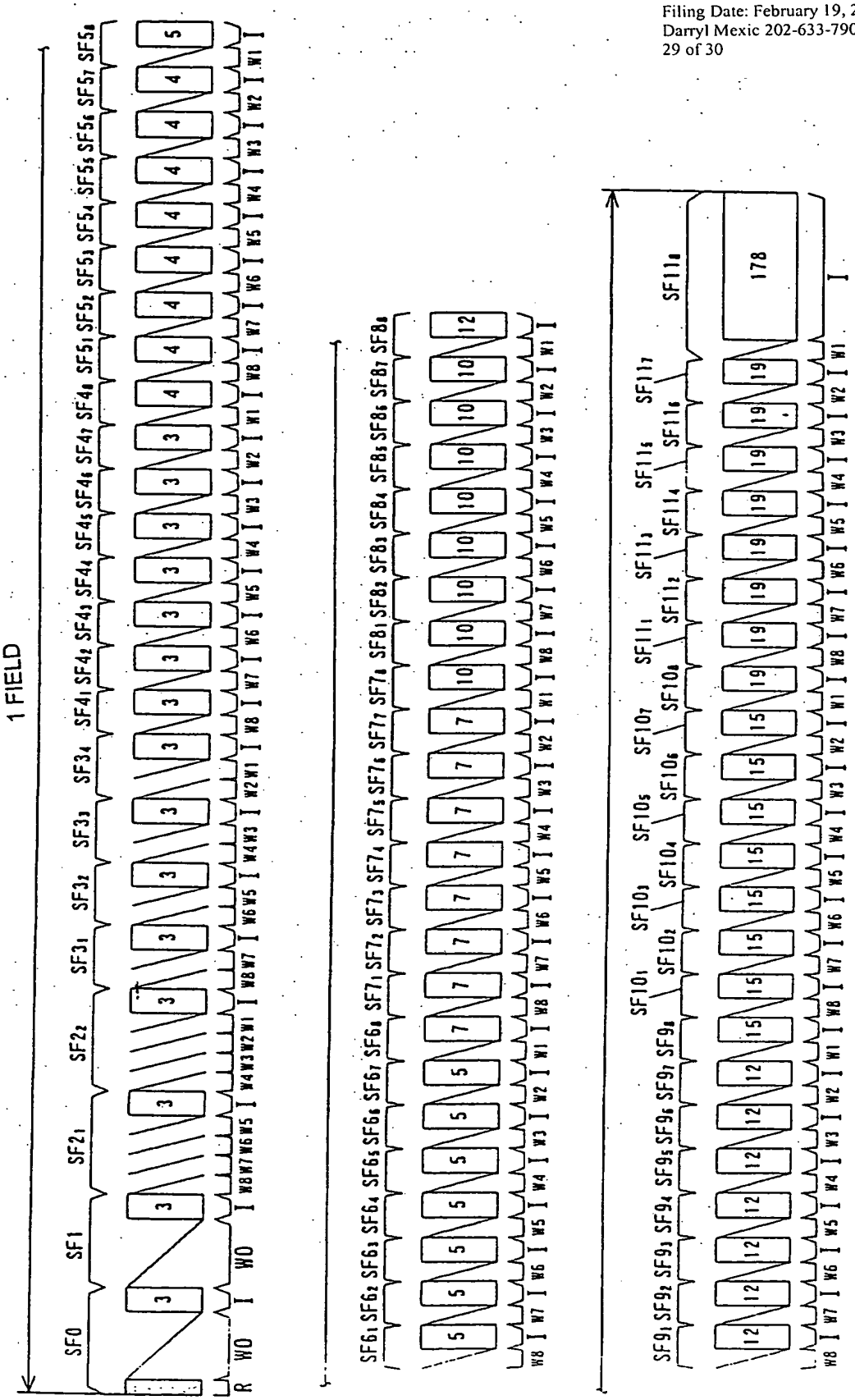
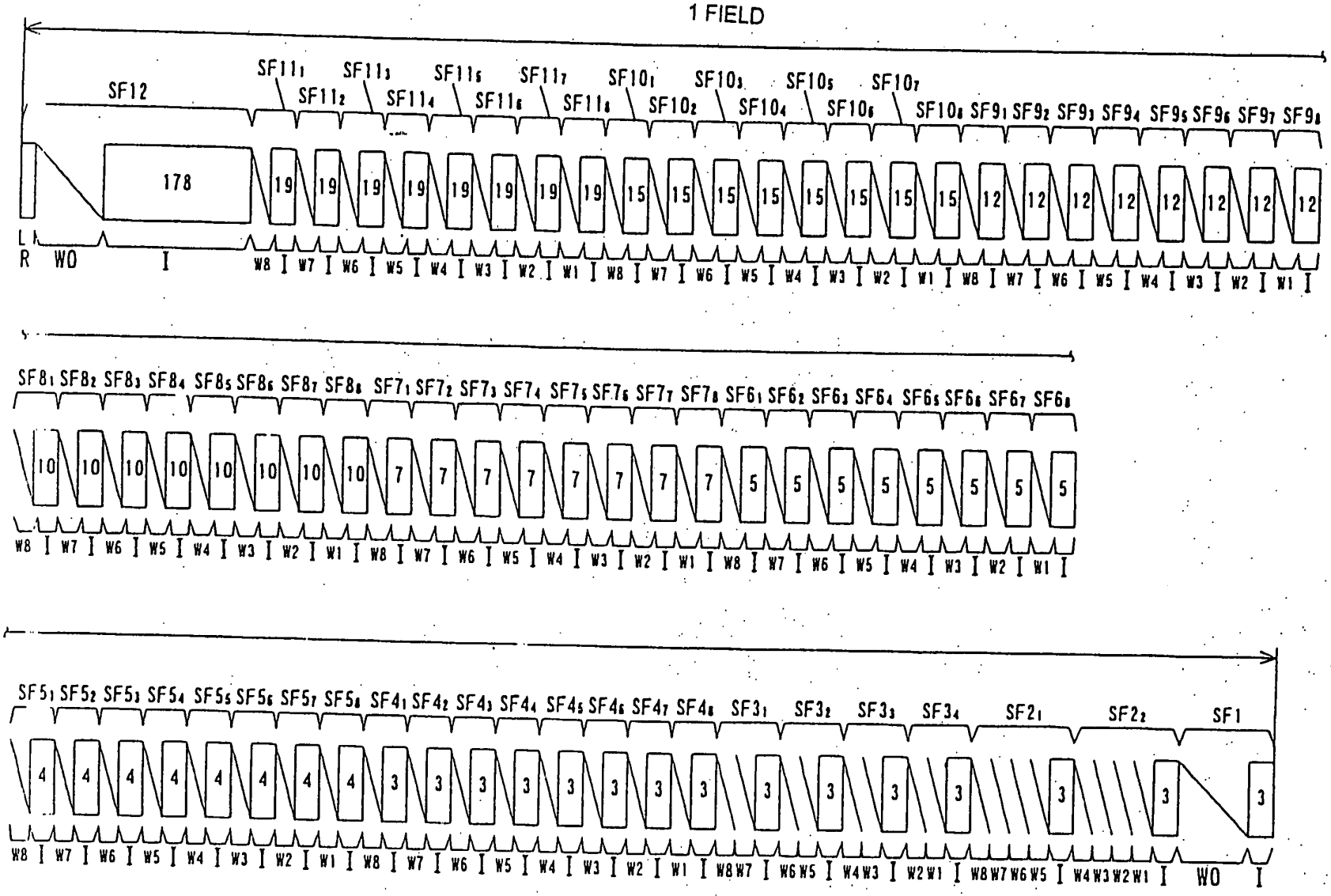


FIG. 32



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